2. System Architecture

Hardware manual

www.pinetek-networks.com

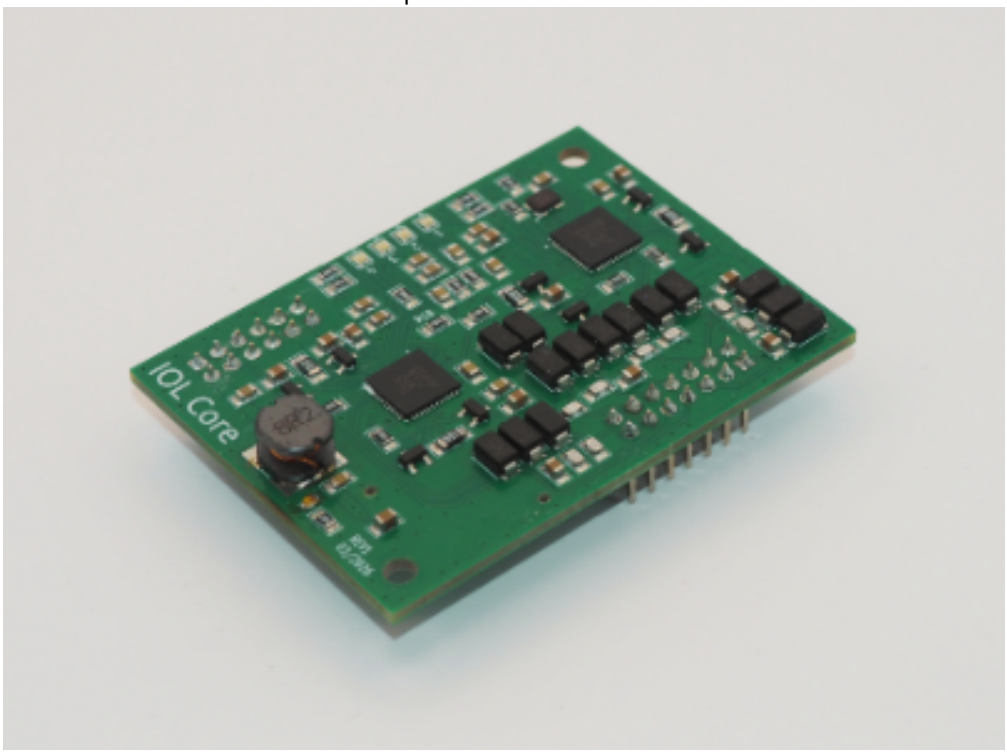
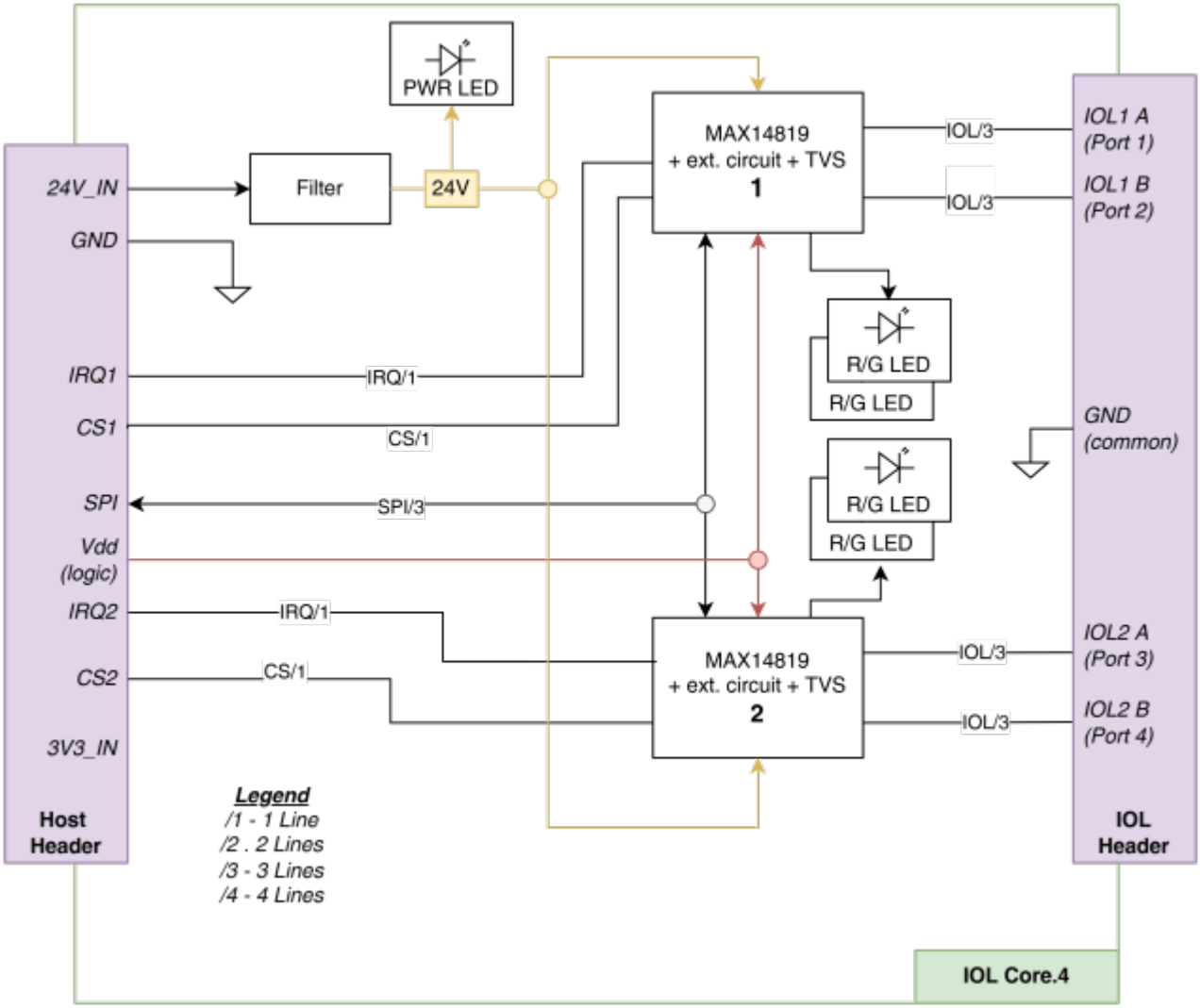


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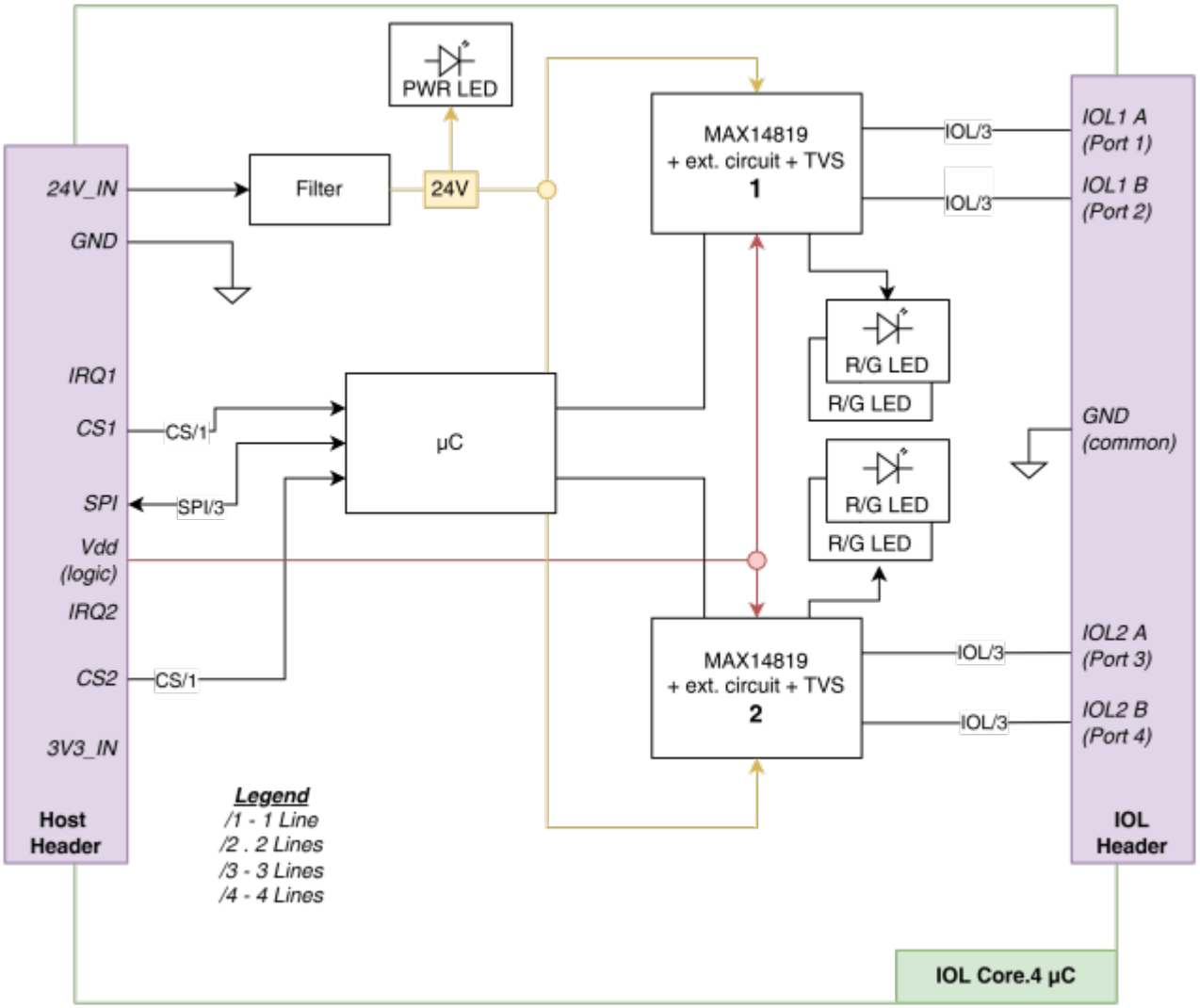
2. System Architecture 5
2.1 IOL Core4 5
2.2 IOL Core4 Pro 5

2. System Architecture

2.1 IOL Core4



2.2 IOL Core4 Pro


PWR LED
24V_IN
GND
Filter
24V
MAX14819
+ ext. circuit + TVS
1
IOL/3
IOL/3
IOL1 A
(Port 1)
IOL1 B
(Port 2)
R/G LED
R/G LED
R/G LED
R/G LED
IRQ1
CS1
CS/1
µC
SPI
SPI/3
Vdd
(logic)
IRQ2
CS2
CS/1
MAX14819
+ ext. circuit + TVS
2
IOL/3
IOL/3
GND
(common)
IOL2 A
(Port 3)
IOL2 B
(Port 4)
3V3_IN
Host
Header
Legend
/1 - 1 Line
/2 . 2 Lines
/3 - 3 Lines
/4 - 4 Lines
IOL
Header
IOL Core.4 µC