2.1 Connections overview

Hardware manual

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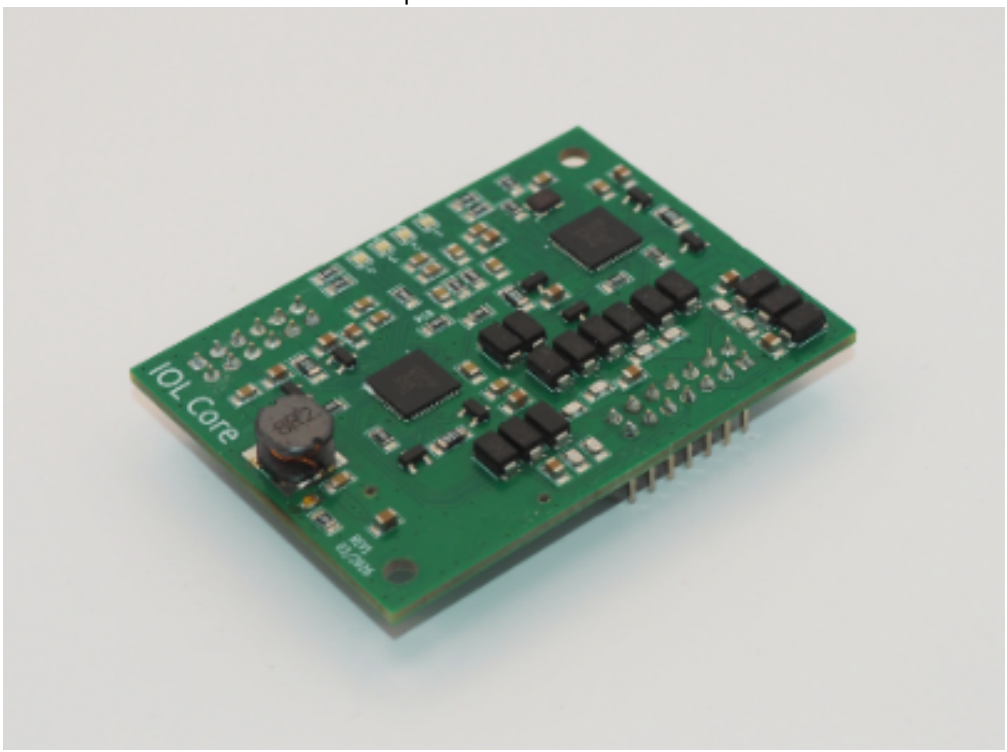
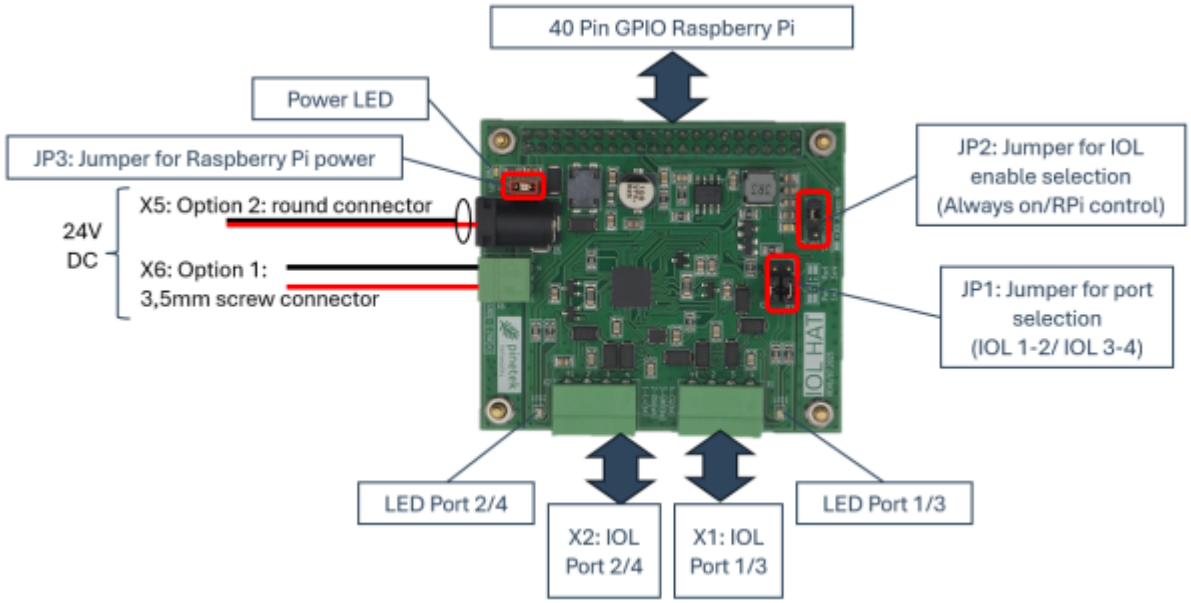


Table of Contents

2.1 Connections overview 5

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The following image provides an overview on the available connections and interfaces:



All logic signals (SPI, interrupts) on the 40-pin GPIO operate on 3V3 logic levels.