3.8 CMD_STATUS3

Hardware manual

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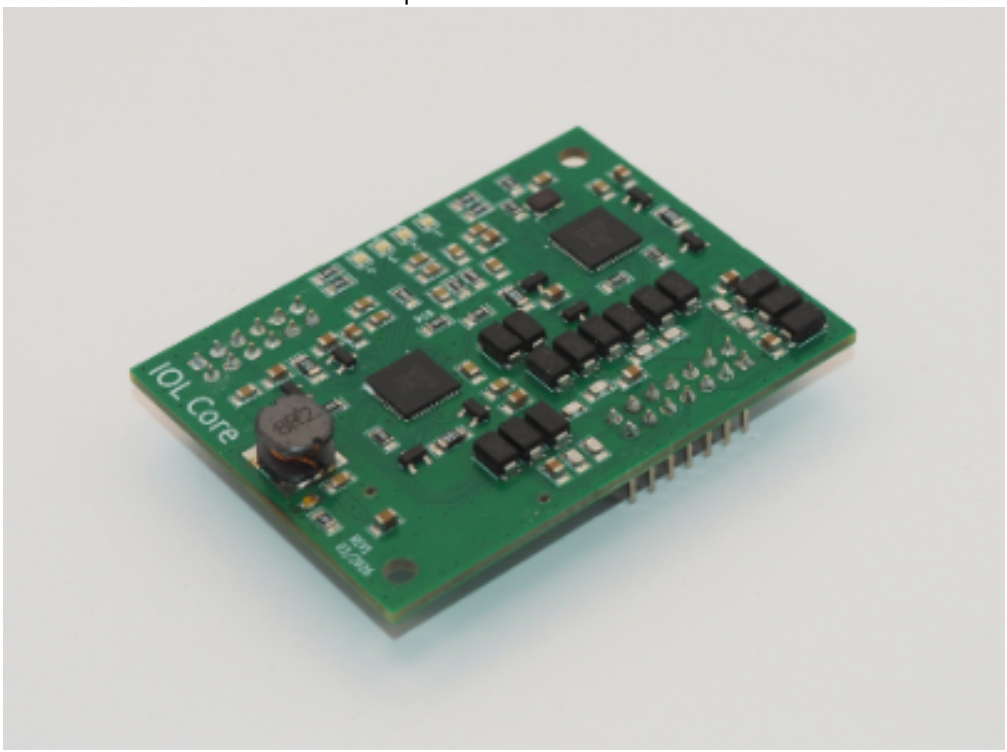


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The status3 command returns the chip-level status register of the MAX14819 transceiver. Unlike CMD_STATUS and CMD_STATUS2, this command is not port-specific and requires only a single command byte (no port index). The returned value is the content of the MAX14819 REG_Status register (0x1E) OR'd with any error bits that were latched since the last call to CMD_STATUS3. Reading this command clears the latch.

Command:

Octet #	Usage
0	Command ID = 0x09

Return Success:

Octet #	Usage	Comment
0	Command ID = 0x09	
1	Chip status	MAX14819 REG_Status (0x1E) OR'd with latched error events. Upper nibble (bits 7..4) contains chip-level diagnostic flags; lower nibble (bits 3..0) contains channel error flags. 0x00 = No errors